TFC Testing

- Using a single PCI interface (A?)
  - loading/reading all memory
  - FRC/STC input reading and processor scheduling (src?)
  - DSP code loading (probably?)
  - with temporary “OS” code, DSP processing (return?)

- Dual PCI connections (A/B)
  - add L2 output
  - real DSP code (“OS”+proc’ing)

- Need MB for obvious things
  - transceivers, monitoring, L3

- Power distribution?